

FORM PTO-1449 (Modified)	OCT 09 2007 U.S. Department of Commerce Patent and Trademark Office INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use Several Sheets If Necessary) (37 CFR § 1.98(b))	Attorney Docket No.: CONX-00800	Serial No.: 10/727,811
		Applicant: Gheorghe Stefan et al.	
		Filing Date: December 4, 2003	Group Art Unit: 2181

U.S. PATENT DOCUMENTS

Examiner Initials	Patent Number/ Publication Number	Issue Date/ Publication Date	Applicant / Patentee	Class	Subclass	Filing Date
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						
AL						
AM						
AN						
AO						

FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
	AP							
	AQ							
	AR							
	AS							
	AT							

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

/BG/	AU	Yamashita et al., "A 3.84 GIPS Integrated Memory Array Processing with 64 Processing Elements and a 2-Mb SRAM," IEEE Journal of Solid-State Circuits, Vol. 29, No. 11, November 1994
	AV	
	AW	
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	BA	
	BB	
	BC	

Examiner: /Benjamin Geib/ Date Considered: 12/12/2007

EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.